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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/619,478	07/19/2000	Shunpei Yamazaki	0756-2187	1882
22204	7590 01/22/2004		EXAMINER	
NIXON PEABODY, LLP 401 9TH STREET, NW			ANYASO, UCHENDU O	
SUITE 900			ART UNIT	PAPER NUMBER
WASINGTO	N, DC 20004-2128		2675	
			DATE MAILED: 01/22/2004	,

Please find below and/or attached an Office communication concerning this application or proceeding.

0 ' 1	Application No.	Applicant(s)			
Advisory Action	09/619,478	YAMAZAKI ET AL.			
•	Examiner	Art Unit			
	Uchendu O Anyaso	2675			
The MAILING DATE of this communication appe	ars on the cover sheet with the o	correspondence address			
THE REPLY FILED 23 December 2003 FAILS TO PLAC Therefore, further action by the applicant is required to avifinal rejection under 37 CFR 1.113 may only be either: (1) condition for allowance; (2) a timely filed Notice of Appeal Examination (RCE) in compliance with 37 CFR 1.114.	oid abandonment of this applica a timely filed amendment which	ation. A proper reply to a n places the application in			
PERIOD FOR RE	EPLY [check either a) or b)]				
a) The period for reply expires 3 months from the mailing date b) The period for reply expires on: (1) the mailing date of this A no event, however, will the statutory period for reply expire I ONLY CHECK THIS BOX WHEN THE FIRST REPLY WAS 706.07(f).	Advisory Action, or (2) the date set forth ater than SIX MONTHS from the mailin	g date of the final rejection.	In		
Extensions of time may be obtained under 37 CFR 1.136(a). The fee have been filed is the date for purposes of determining the period of fee under 37 CFR 1.17(a) is calculated from: (1) the expiration date of (2) as set forth in (b) above, if checked. Any reply received by the Official timely filed, may reduce any earned patent term adjustment. See 37 C	of extension and the corresponding amount the shortened statutory period for reply be later than three months after the mai	unt of the fee. The appropriate extension originally set in the final Office action; or	on		
 A Notice of Appeal was filed on Appellant's 37 CFR 1.192(a), or any extension thereof (37 CFF 					
2. The proposed amendment(s) will not be entered be	ecause:				
(a) they raise new issues that would require further	er consideration and/or search (see NOTE below);			
(b) they raise the issue of new matter (see Note b	elow);				
(c) they are not deemed to place the application in issues for appeal; and/or	n better form for appeal by mate	rially reducing or simplifying the	!		
(d) they present additional claims without canceli	ng a corresponding number of f	inally rejected claims.			
NOTE:					
3. Applicant's reply has overcome the following reject	ion(s):				
4. Newly proposed or amended claim(s) would canceling the non-allowable claim(s).	be allowable if submitted in a se	eparate, timely filed amendment			
5.⊠ The a)□ affidavit, b)□ exhibit, or c)⊠ request for application in condition for allowance because: <u>See</u>		dered but does NOT place the			
6. The affidavit or exhibit will NOT be considered becaraised by the Examiner in the final rejection.	ause it is not directed SOLELY t	o issues which were newly			
7. For purposes of Appeal, the proposed amendment explanation of how the new or amended claims we					
The status of the claim(s) is (or will be) as follows:					
Claim(s) allowed:					
Claim(s) objected to:					
Claim(s) rejected: <u>1-32</u> .					
Claim(s) withdrawn from consideration:					
8. The drawing correction filed on is a) applied applied on is a)	roved or b) disapproved by t	he Examiner.			
9. Note the attached Information Disclosure Statemer	nt(s)(PTO-1449) Paper No(s)				
O. Note the attached Information Disclosure Statement(s)(PTO-1449) Paper No(s). O. Other: CHANH NGUYEN					
		CHANH NGHYEN			
		PRIMARY EXAMINER			

4. . .

Continuation of 5. does NOT place the application in condition for allowance because: Regarding independent claims 1, 6, 11 and 14, Applicant contends that Sawada fails to teach, disclose, or suggest a digital video signal dividing circuit and a controller feeding pulses to the display panel. Furthermore, Applicant argues that Kuwabara merely teaches a digital video signal dividing circuit without any suggestion or motivation to include a controller feeding pulses to a display panel. Examiner disagrees with Applicant's assertions for at least the following reasons: First, Sawada teaches a control circuit in the form of a display mode dependence controller (17) that is connected to the digital image processor 3 and the scanning control circuit 22 (figure 1 at 3, 17). While, Sawada does not show the controller 17 feeding directly to display unit 4, the connection in Sawada of the controller 17 to the scanning control circuit 22 reads directly on applicant's circuit connectivity as shown in figure 1 (Compare figure 1 of Applicant and Sawada). The motivation for doing so would have been to provide a display device which can display an image in correspondence with various display modes (column 2, lines 40-45), and to achieve a scanning control circuit 22 that changes the scanning method in accordance with an instruction from the display mode dependence controller 17 in correspondence with the display mode (column 4, lines 40-43. Second, while Sawada does not teach explicitly a digital video signal dividing circuit, Kuwabara teaches this concept by teaching a driving method for an active matrix type image display having a plurality of video signal lines wherein a scheme is designed to achieve a digital video dividing circuit of an original video signal (column 12, lines 10-27, figure 7). The motivation for combining Sawada and Kuwabara would have been to prevent degradation in the display quality (column 12, lines 61-67 through column 13, line 4, figure 7). As such, this application in its current form is not in a condition for allowance.

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